



501.20699VC1
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: R. HORI
Serial No.: 07/458,507
Filed: December 28, 1989
For: SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: 236
Examiner: S. Baker

6/C
12/13/90
mgc

AMENDMENT

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

November 27, 1990

Sir:

The following is in response to the Office Action dated July 27, 1990.

IN THE TITLE:

On the cover page, both occurrences and page 1, line 2, delete the present title, and insert the following new title:

SEMICONDUCTOR INTEGRATED CIRCUIT WITH VOLTAGE LIMITER HAVING DIFFERENT OUTPUT RANGES FOR NORMAL OPERATION AND PERFORMING OF AGING TESTS.

IN THE SPECIFICATION:

Page 1, line 6, insert -- to drive circuits on the chip having small geometries -- after "chip";

line 8, delete "attended with" and insert -- accompanied by a --;

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